n	Fanout	Storage in bits $T_{n,g}$
2	16	$\lceil \frac{S}{g} \rceil * 2^p * (g + g + 32p)$
4	4	$\lceil \frac{S}{g} \rceil * 2^p * (g + 3g + 16p)$
8	2	$\lceil \frac{S}{g} \rceil * 2^p * (g + 7g + 16p)$

Table 2: Optimal Module Sizes. $\lceil log_2(gL) \rceil$ is denoted by p for clarity.

Errata from "A High Throughput String Matching Architecture for Intrusion Detection and Prevention"

In Table 2 page 7 of the original paper, there should be no n in all three formula in the third column. The values of n, e.g. 16, 4, and 2, are already plugged in in these formula.

The correct table therefore should be as above.